Docket No: 740756-1614

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Takeshi FUKUNAGA et al.

Application Serial No.: 08/781,920

Filed: December 30, 1996

For: METHOD OF FABRICATING A

Docket No: 740756-1614

Group Art Unit: 1762

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Examiner: M. PADGETT

July

Application Serial No.: 08/781,920

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on December 21, 2001.

Deborah Tsepal

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents Washington, DC 20231

SEMICONDUCTOR DEVICE UTILIZING

A CATALYST MATERIAL SOLUTION

Sir:

In accordance with the duty of disclosure as set forth in 37 C.F.R. §1.56, Applicants hereby submit the following information in conformance with 37 C.F.R. §§ 1.97 and 1.98. Pursuant to 37 C.F.R. § 1.98, a copy of each of the documents cited are enclosed.

The undersigned certifies that each item of information contained in this information disclosure statement was first cited in any communication from a foreign patent office, in connection with a counterpart foreign application not more than three (3) months prior to the filing of this statement.

It is requested that the accompanying information disclosure statement be considered and made of record in the above-captioned application. To assist the Examiner, the documents are listed

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on the attached form PTO-1449. It is respectfully requested that an Examiner initialed copy of this form be returned to the undersigned.

The Commissioner is hereby authorized to charge any fees connected with this filing which may be required now, or credit any overpayment to Deposit Account No. 19-2380.

Respectfully submitted,

James E. Howard

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